

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET MCH6602 — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance
- · Ultrahigh-speed switching
- 1.5V drive
- · Composite type with 2 MOSFETs contained in a single package, facilitating high-density mounting

Specifications

Absolute Maximum Ratings at Ta=25°C

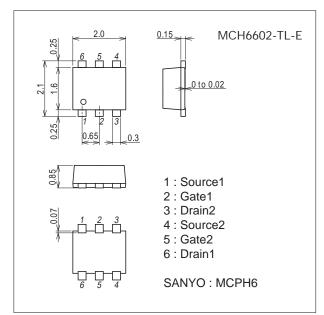
| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|--------|---|-------------|------|
| Drain-to-Source Voltage | VDSS | | 30 | V |
| Gate-to-Source Voltage | VGSS | | ±10 | V |
| Drain Current (DC) | ID | | 0.35 | А |
| Drain Current (Pulse) | IDP | PW≤10μs, duty cycle≤1% | 1.4 | А |
| Allowable Power Dissipation | PD | When mounted on ceramic substrate (900mm ² ×0.8mm) 1unit | 0.8 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

This product is designed to "ESD immunity $< 200V^*$ ", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ) 7022A-006



Product & Package Information

- Package : MCPH6 • JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

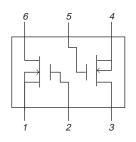
Packing Type : TL





Marking

Electrical Connection

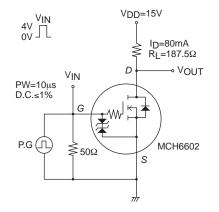


SANYO Semiconductor Co., Ltd. http://semicon.sanyo.com/en/network

| Electrical | Characteristics at Ta | =25°C |
|------------|-----------------------|-------|
|------------|-----------------------|-------|

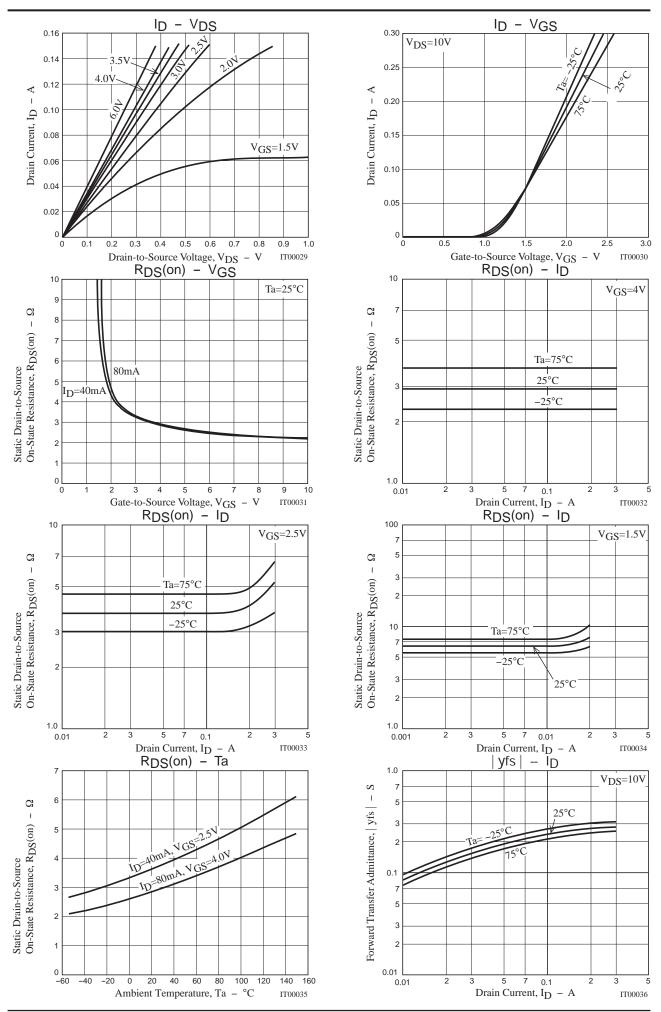
| Decemeter | Cumbol | Canditiana | | Ratings | | |
|--|-----------------------|---|------|---------|------|------|
| Parameter | Symbol | Conditions | min | typ | max | Unit |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | ID=1mA, VGS=0V | 30 | | | V |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =30V, V _{GS} =0V | | | 1 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±8V, V _{DS} =0V | | | ±10 | μΑ |
| Cutoff Voltage | V _{GS} (off) | V _{DS} =10V, I _D =100μA | 0.4 | | 1.3 | V |
| Forward Transfer Admittance | yfs | V _{DS} =10V, I _D =80mA | 0.15 | 0.22 | | S |
| Static Drain-to-Source On-State Resistance | R _{DS} (on)1 | ID=80mA, VGS=4V | | 2.9 | 3.7 | Ω |
| | R _{DS} (on)2 | ID=40mA, VGS=2.5V | | 3.7 | 5.2 | Ω |
| | R _{DS} (on)3 | ID=10mA, VGS=1.5V | | 6.4 | 12.8 | Ω |
| Input Capacitance | Ciss | | | 7.0 | | рF |
| Output Capacitance | Coss | VDS=10V, f=1MHz | | 5.9 | | рF |
| Reverse Transfer Capacitance | Crss | | | 2.3 | | рF |
| Turn-ON Delay Time | t _d (on) | | | 19 | | ns |
| Rise Time | tr | | | 65 | | ns |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit. | | 155 | | ns |
| Fall Time | tf | - | | 120 | | ns |
| Total Gate Charge | Qg | | | 1.58 | | nC |
| Gate-to-Source Charge | Qgs | V _{DS} =10V, V _{GS} =10V, I _D =150mA | | 0.26 | | nC |
| Gate-to-Drain "Miller" Charge | Qgd | 1 | | 0.31 | | nC |
| Diode Forward Voltage | V _{SD} | IS=150mA, VGS=0V | | 0.87 | 1.2 | V |

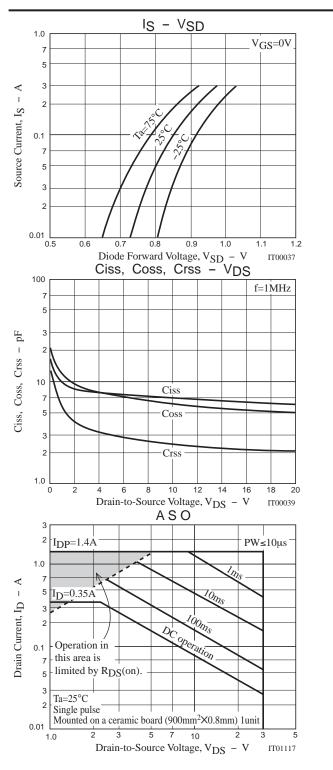
Switching Time Test Circuit

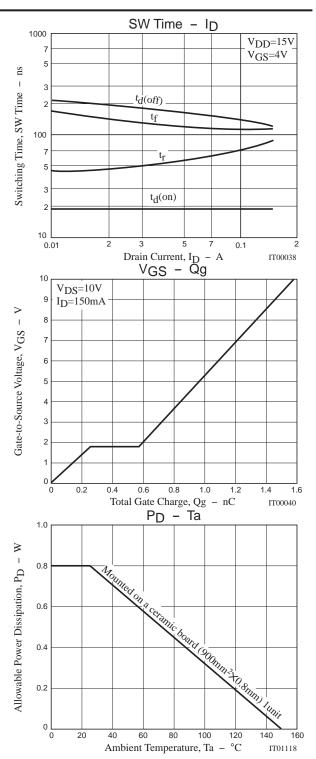


Ordering Information

| Device | Package | Shipping | memo | |
|--------------|---------|----------------|---------|--|
| MCH6602-TL-E | MCPH6 | 3,000pcs./reel | Pb Free | |







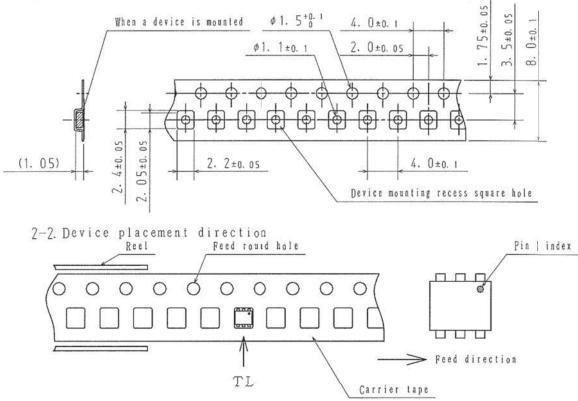
Taping Specification MCH6602-TL-E

1. Packing Format

| Package Name | Carrier Tape | | mun Numbe s contain | | Packing format | | |
|--------------|-----------------------------|--------|------------------------|---------------------------------|--|---|--|
| Туре | | Reel | [aner box | Outer box | Inner BOX (C-1 |) Outer BOX (A-7) | |
| MCPH6 | MCP4 | 3, 000 | 15, 000 | 90,000 | 5 reels contained Dimensions:mm (exter | 요즘 것 같은 것 같은 것 같은 것을 가지 않는 것 같이 많이 | |
| |] | | | | 183×72×18 | 5 440×195×210 | |
| Packing me | thod | | Keel | (u 1 | ıit:mm) Î M | uter box label t is a label at the time of factory shipme he form of a label may change in plysical istribution process. | |
| â | A A | | | | <u>; 9</u> | 108 | |
| | Type | No. | | | | TYPE CODE +CCCCCCCCCCCC+ | |
| | MILL LOT I [777777] Quan | | | | | TYPE COCCOCCOCCOCCOCCOCCOCCOCCOCCOCCOCCOCCOC | |
| | Will Wuali | iiiy | (2) | SPECIAL | | 107 00000000 | |
| | Orig | in | | * Z) 7 2 2 (SEMBLY: **** (| DIFFUSION: ****) | | |
| | \ | | NOTE | : (1) | | *20722005310C* ASSEVELY:**** (DIFFUSION:****) | |
| | Reel la | bel | Th | e LEAD FI | REE & description show of the terminal is lea | | |
| | | | | La b e l | | e | |
| | | | | LEAD FRE | obiin incor | | |
| | | | | LEAD FRE | CE 4 JEITA Phase | 3 | |

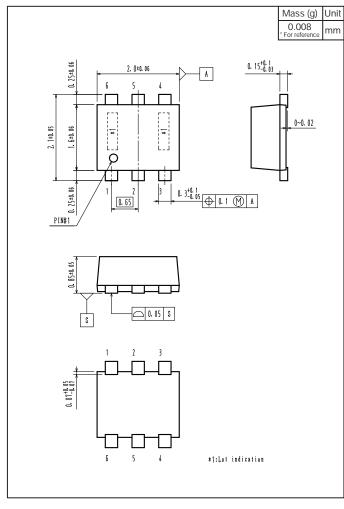
2. Taping configuration

2-1. Carrier tape size (unit:mm)

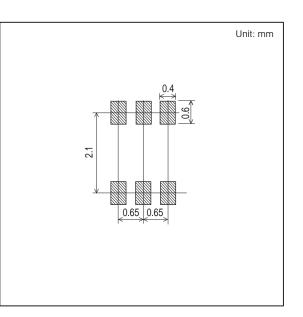


Those with pin 1 index on the feed hole side TL

Outline Drawing MCH6602-TL-E



Land Pattern Example



Note on usage : Since the MCH6602 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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